



PATENT

IN THE UNITED STATES PATENT OFFICE

Applicants: L. James Hwang et al.
Assignee: Xilinx, Inc.
Title: "A Method and System for Modeling and Automatically Generating an Electronic Design from a System-Level Environment"
Serial No.: 09/981,503 Filing Date: 10/16/2001
Examiner: Thai Q. Phan Art Unit: 2128
Docket No.: X-953 US Conf. No.: 6920

Mail Stop AF
COMMISSIONER FOR PATENTS
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Alexandria, Virginia 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicant[s] bring to the attention of the Examiner the ten (10) references listed in the attached Substitute for Form 1449A/PTO. A copy of each is enclosed herein.


The enclosed references are being cited after the receipt of a first office action but prior to any notice of allowability.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

The Commissioner is hereby authorized to charge the fee of \$180.00 due under 1.17(p) and any additional fees, or credit any overpayment which may be required to Deposit Account No. 24-0040.

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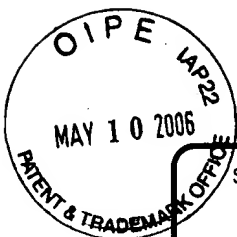
Respectfully submitted,


Kim Kanzaki, Ph.D.
Attorney for Applicants
Reg. No. 37,652

I hereby certify that this correspondence is being deposited with the United States Postal Service as **first class mail** in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on May 8, 2006.

Pat Tompkins
Name


Signature



Substitute for form 1449A/PTO		Complete if Known	
		Application / Conf. No.	09/981,503 / 6920
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Filing Date	October 16, 2001
		First Named Inventor	L. James Hwang
		Art Unit	2128
		Examiner Name	Thai Q. Phan
		Attorney Docket Number	X-953 US
Sheet	1	of	1

OTHER -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Chris Dick et al.; "Configurable Logic for Digital Communications: Some Signal Processing Perspectives"; Copyright 1999 IEEE; IEEE Communications Magazine; August 1999; Vol. 2.; pps. 107-111.	
		Chris H. Dick et al.; "Design and Implementation of High-Performance FPGA Signal Processing Datapaths for Software Defined Radios"; Embedded Systems Conference; April 2001; pps. 1-16.	
		Maya Gokhale et al.; "Stream-Oriented FPGA Computing in the Streams-C High Level Language"; Copyright 2000 IEEE; in Proceedings FCCM00; IEEE Computer Society Press 2000; pps. 49-56.	
		Jennifer Eyre; "The Digital Signal Processor Derby"; IEEE Spectrum; June 2001; pps. 62-68.	
		Michael R. Sturgill et al.; "Design and Verification of Third Generation Wireless Communication Systems"; White Paper, Cadence Design Systems, Inc.; December 18, 2000; pp. 1-10.	
		Edward A. Lee; "What's Ahead for Embedded Software?"; Copyright 2000 IEEE; IEEE Computer; Vol. 33, No. 9; September 2000; pps. 18-26.	
		Benjamin Levine et al.; "Mapping of an Automated Target Recognition Application from a Graphical Software Environment to FPGA-based Reconfigurable Hardware"; in Proceedings FCCM99; IEEE Computer Society Press; 1999; pps. 292-293.	
		Roger Lagadec et al.; "A 2-Channel, 16-Bit Digital Sampling Frequency Converter for Professional Digital Audio"; Proceedings IEEE International Conference ASSP; Copyright 1982 IEEE; April 1982; pps. 93-96.	
		Stanley A. White; "Applications of Distributed Arithmetic to Digital Signal Processing: A Tutorial Review"; IEEE ASSP Magazine; Vol. 6(3); July 1989; Copyright 1989 IEEE; pps. 4-19.	
		M. Schiff; "Baseband Simulation of Communications System"; Application Note AN133; Elanix, Inc.; April 2000; pps. 1-5.	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

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PTO/SB/17 (10-02)
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FEE TRANSMITTAL for FY 2005

Patent fees are subject to annual revision

TOTAL AMOUNT OF PAYMENT (\$)**180.00**

Complete if Known

Application / Conf. No.	09/981,503 / 6920
Filing Date	October 16, 2001
First Named Inventor	L. James Hwang
Examiner Name	Thai Q. Phan
Art Unit	2128
Attorney Docket No.	X-953 US

METHOD OF PAYMENT (check one)

1. ☒ The Commissioner is hereby authorized to charge indicated fees, any additional fees required, and credit any over payments to:

☒ Deposit Account

Deposit
Account
Number

24-0040

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XILINX, INC.

FEE CALCULATION

1. BASIC FILING FEE

Large Entity

Fee Code	Fee (\$)	Fee Description	Fee
1001	770	Utility filing fee	
1002	330	Design filing fee	
1003	510	Plant filing fee	
1004	790	Reissue filing fee	
105	160	Provisional filing fee	

SUBTOTAL (1) (\$)

2. EXTRA CLAIM FEES FOR UTILITY AND REISSUE

Total Claims	-20** =	Extra	X	Fee from below	=	Fee Paid
Indep. Claims	- 3** =		X		=	
Multiple Dependent Claims			X		=	

**or number previously paid, if greater; For Reissues, see below

Large Entity

Fee Code	Fee (\$)	Fee Description
1202	18	Claims in excess of 20
1201	86	Independent claims in excess of 3
1203	290	Multiple dependent claim, if not paid
1204	86	**Reissue independent claims over original patent
1205	18	**Reissue claims in excess of 20 and over original patent

SUBTOTAL (2) (\$)

FEE CALCULATION (continued)

3. ADDITIONAL FEES

Large Entity
Fee Code

Fee (\$)

Fee Description

Fee Paid

1051	130	Surcharge - late filing fee or oath	
1052	50	Surcharge - late provisional filing fee or cover sheet.	
1812	2,520	For filing a request for exparte reexamination	
1804	920*	Requesting publication of SIR prior to Examiner action	
1805	1,840*	Requesting publication of SIR after Examiner action	
1251	120	Extension for reply within first month	
1252	450	Extension for reply within second month	
1253	1020	Extension for reply within third month	
1254	1,530	Extension for reply within fourth month	
1255	2,080	Extension for reply within fifth month	
1401	500	Notice of Appeal	
1402	500	Filing a brief in support of an appeal	
1403	1000	Request for oral hearing	
1451	1,510	Petition to institute a public use proceeding	
1452	110	Petition to revive - unavoidable	
1453	1,500	Petition to revive - unintentional	
1501	1,400	Utility issue fee (or reissue)	
1460	130	Petitions to the Commissioner	
1807	50	Petitions related to provisional applications	
1806	180	Submission of Information Disclosure Stmt	\$180
8021	40	Recording each patent assignment per property (times number of properties)	
1809	790	Filing a submission after final rejection (37 CFR 1.129(a))	
1810	790	For each additional invention to be examined (37 CFR 1.129(b))	
1801	790	Request for Continued Examination (RCE)	

Other fee (specify)

*Reduced by Basic Filing Fee Paid SUBTOTAL (3) (\$)**180.00**

SUBMITTED BY

Complete (if applicable)

Name (Print/Type)	Kim Kanzaki	Registration No. (Attorney/Agent)	37,652	Telephone	408-879-6149
Signature				Date	05-08-2006

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